



DATA SHEET

LOW OHMIC CHIP RESISTORS

RL series 5%, 2%, 1% sizes 0402/0603/0805/1206/ 1210/1218/2010/2512

RoHS compliant





YAGEO Phicomp

9

SCOPE

This specification describes RL0402 to RL2512 low ohmic chip resistors with lead-free terminations made by thick film process.

APPLICATIONS

- Converters
- Printer equipment
- Server board
- Telecom
- Consumer

FEATURES

- RoHS compliant
 - Products with lead free terminations meet RoHS requirements
 - Pb-glass contained in electrodes
 - Resistor element and glass are exempted by RoHS
- Reducing environmentally hazardous wastes
- High component and equipment reliability
- Saving of PCB space
- None forbidden-materials used in products/production
- Low resistances applied to current sensing
- Halogen Free Epoxy

ORDERING INFORMATION - GLOBAL PART NUMBER & 12NC

Both part numbers are identified by the series, size, tolerance, packing type, temperature coefficient, taping reel and resistance value.

YAGEO BRAND ordering code

GLOBAL PART NUMBER (PREFERRED)

RL XXXX X X X XX XXXX L

(I)	(2)	(3)	(4)	(5)	(6)	(7)

(I) SIZE

0402 / 0603 / 0805 / 1206 / 1210 / 1218 / 2010 / 2512

(2) TOLERANCE

 $F = \pm 1\%$ $G = \pm 2\%$

 $J = \pm 5\%$

(3) PACKAGING TYPE R = Paper taping reel

K = Embossed taping reel

(4) TEMPERATURE COEFFICIENT OF RESISTANCE

- = Base on spec

(5) TAPING REEL

07 = 7 inch dia, Reel

- 10 = 10 inch dia. Reel
- 13 = 13 inch dia. Reel

(6) RESISTANCE VALUE

There are 2~4 digits indicated the resistor value. Letter R/K/M is decimal point, no need to mention the last zero after R/K/M, e.g. I K2, not I K20.

Detailed resistance rules show in table of "Resistance rule of global part number".

(7) OPTIONAL CODE

L = optional symbol (Note) H = Halogen / Lead free (special code on request)

Resistance	rul	e of	glo	bal	part
numbor					

number	
Resistance code rule	e Example
	$ORI = 0.1 \Omega$
ORXXX	$0R12 = 0.12 \Omega$
(1 to 976 mΩ)	$0R105 = 0.105 \Omega$
VDVV	$ R = \Omega $
XRXX	irs = 1.5 Ω
(1 to 9.76 Ω)	9R76 = 9.76 Ω
XXRX	$IOR = IO \Omega$
(10 to 97.6 Ω)	97R6 = 97.6 Ω
XXXR (100 to 976 Ω)	100R = 100 Ω
XKXX	IK = 1,000 Ω
(Ι to 9.76 K Ω)	9K76 = 9760 Ω
XMXX	$IM = I,000,000 \Omega$
(I to 9.76 MΩ)	$9M76 = 9,760,000 \Omega$

ORDERING EXAMPLE

The ordering code of a RL0603 chip resistor, value 0.56 Ω with ±1% tolerance, supplied in 7-inch tape reel is: RL0603FR-070R56 (L).

NOTE

- I. All our RSMD products meet RoHS compliant. "LFP" of the internal 2D reel label mentions "Lead Free Process"
- 2. On customized label, "LFP" or specific symbol printed and the optional "L" at the end of GLOBAL PART NUMBER / I2NC can be added (both are on customer request)
- 3. Comply Halogen Free Epoxy

Chip Resistor Surface Mount RL SERIES 0402 to 2512 (RoHS Compliant) 9

PHYCOMP BRAND ordering codes

Both GLOBAL PART NUMBER (preferred) and I2NC (traditional) codes are acceptable to order Phycomp brand products.

GLOBAL PART NUMBER (PREFERRED)

For detailed information of GLOBAL PART NUMBER and ordering example, please refer to page 2.

12NC CODE

2350) / 2390 / (I)	2322	_	(2) (3) (4)				Last dig Resistance	git of 12N decade ⁽³		Last digit
SIZE	TYPE	START	TOL. (%)	resistance Range	EMBOSSED ⁽²⁾ TAPE ON REEL		REEL (units)	0.01 to 0.0 0.1 to 0.97			0 7
0.400		0050	. 50/		4,000	5,000	10,000	l to 9.76 🤉	2		8
0402	LRC31	2350		0.05 to Ω	-	-	513 20xxx	10 to 97.6	Ω		9
	LRC32	2350		0.05 to Ω	-	-	513 22xxx	100 to 976	δΩ		
0603	LRC21	2350		0.01 to 1 Ω	-	512 10xxx	-	l to 9.76 k	Ω		2
	LRC22	2350	±1%	0.01 to 1 Ω	-	512 12xxx	-	10 to 97.6	kΩ		3
0805	LRCII	2350	±5%	0.01 to 1 Ω	-	511 10xxx	-	100 to 976	δkΩ		4
	LRC12	2350	±1%	0.01 to 1 Ω	-	511 12xxx	-	l to 9.76 N	γΩ		5
1206	LRC01	2350	±5%	0.01 to 1 Ω	-	510 10xxx	-	10 to 97.6	MΩ		6
	LRC02	2350	±1%	0.01 to 1 Ω	-	510 12xxx	-				
1210	LPRC101	2390	±5%	0.01 to 0.0976 Ω	-	735 90xxx	-	Example:	0.02 Ω	=	0200 or 200
	LPRC101	2390	±5%	0.1 to 1 Ω	-	735 60xxx	-		0.3 Ω	=	3007 or 307
	LPRC102	2390	±1%	0.01 to 1 Ω	-	735 3xxxx	-		ΙΩ	=	1008 or 108
1218	LPRC201	2322	±5%	0.0 to Ω	735 64xxx	-	-		33 kΩ	=	3303 or 333
	LPRC201	2322	±1%	0.01 to 1 Ω	735 7xxxx	-	-		10 MΩ	=	1006 or 106
2010	LPRCIII	2322	±5%	0.01 to 0.0976 Ω	760 90xxx	-	-	ORDERING	G EXAMF	'LE	
	LPRCIII	2322	±5%	0.1 to 1 Ω	760 60xxx	-	-	The orderi	ng code o	of a F	RL0603 chip
	LPRCIII		±1%	0.01 to 0.0976 Ω	761 90xxx	_	_	resistor, value 0.56 Ω with ±1%			
	LPRCIII			0.1 to 1 Ω	761 6xxxx	-	-	tolerance, s	••	•	
2512	LPRC221			0.01 to 0.0976 Ω	762 90xxx	_	_	units per re or RL0603			• • •
	LPRC221			0.1 to 1 Ω	762 60xxx		_			0(L).	,
	LPRC221		±1%		762 00xxx 763 90xxx	-	-	NOTE			
				0.01 to 0.0976 Ω		-	-	I. All our RS			
	LPRC221	2322	±1%	0.1 to 1 Ω	763 6xxxx	-	-	compliant.	"LFP" of th	ie inte	ernal 2D reel

(1) The resistors have a 12-digit ordering code starting with 2350/2390/2322.

- (2) The subsequent 4 or 5 digits indicate the resistor tolerance and packaging. (In I2NC code, only 07" tape reel code is supplied. Supply of 10"/13" tape reel is requested in Global part number ordering code.)
- (3) The remaining 4 or 3 digits represent the resistance value with the last digit indicating the multiplier as shown in the table of "Last digit of I2NC".
- (4) "L" is optional symbol (Note).

"H" is Halogen / Lead free (special code on request).

- label mentions "Lead Free Process" 2. On customized label, "LFP" or specific symbol printed and the optional "L" at the end of GLOBAL PART NUMBER / 12NC can be added (both are on
- customer request) 3. Comply Halogen Free Epoxy

YAGEO	Phicomp						Product specifica	tion 🖌
	Chip Resistor	Surface Mount	RL S	ERIES	0402 to 2512 (RoH	S Compliant)		
1 <u>ARKING</u>								
	206 / RLI2I0 /RLI	218 / RL2010 / RL25	12					
R	N2N	E-24 series / Nor	n-series (R= 25/	/40/50/60/250/40	00/500 mΩ): 4	digits	
Fig. I Val	$ue = 20 \text{ m}\Omega$	The "R" is used a	as a decin	nal poi	int; the other 3 o	ligits are signi	ficant.	
RL0603: R≥1(0 mΩ IN E-24 SEI	RIES, $R = 10/20/30/40$) /50/60 mΩ	2				
	R22	3 digits						
	$re = 22 \text{ m}\Omega$	The "R" is used a	as a decin	nal poi	int; the other 2 o	ligits are signi	ficant.	
RL0402 / RL0	603: R<100 mΩ E	XCEPT 10/20/30/40/5	50/60 mΩ					
		No marking						
Fig. 3	predd?							

For further marking information, please see special data sheet "Chip resistors marking".

CONSTRUCTION

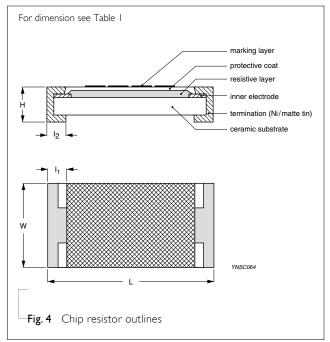
The resistors are constructed out of a high-grade ceramic body. Internal metal electrodes are added at each end and connected by a resistive paste. The composition of the paste is adjusted to give the approximate required resistance and laser cutting of this resistive layer that achieves tolerance trims the value. The resistive layer is covered with a protective coat and printed with the resistance value. Finally, the two external terminations (matte tin) are added. See fig. 4.

DIMENSIONS

Dec 11,2008 V.4

TYPE	L (mm)	W (mm)	H (mm)	l₁ (mm)	l2 (mm)
RL0402	1.00 ±0.10	0.50 ±0.05	0.35 ±0.05	0.20 ±0.10	0.25 ±0.10
RL0603	1.60 ±0.10	0.80 ±0.10	0.45 ±0.10	0.25 ±0.15	0.25 ±0.15
RL0805	2.00 ±0.10	1.25 ±0.10	0.50 ±0.10	0.35 ±0.20	0.35 ±0.20
RL1206	3.10 ±0.10	1.60 ±0.10	0.55 ±0.10	0.45 ±0.20	0.40 ±0.20
RL1210	3.10 ±0.10	2.60 ±0.15	0.55 ±0.10	0.50 ±0.20	0.50 ±0.20
RL1218	3.05 ±0.15	4.60 ±0.20	0.55 ±0.10	0.45 ±0.25	0.50 ±0.25
RL2010	5.00 ±0.10	2.50 ±0.15	0.55 ±0.10	0.60 ±0.20	0.50 ±0.20
RL2512	6.35 ±0.10	3.20 ±0.15	0.55 ±0.10	0.60 ±0.20	0.50 ±0.20

OUTLINES



www.yageo.com

ELECTRICAL CHARACTERISTICS

 Гаb	le	2
 au	i C	-

TYPE / RESISTANCE RANGE		TEMPERATURE COEFFICIENT OF RESISTANCE								
RL0402 50mΩ≤R<1Ω				50mΩ	2≤R <iω< th=""><th></th><th></th></iω<>					
				±800	ppm/°C					
PLOGOZ LOW OFREIO	I0mΩ≤R≤36	6mΩ 36	mΩ <r:< th=""><th>≤9ImΩ</th><th>91mΩ<f< th=""><th>R≤500mΩ</th><th>500mΩ<r<iω< th=""></r<iω<></th></f<></th></r:<>	≤9ImΩ	91mΩ <f< th=""><th>R≤500mΩ</th><th>500mΩ<r<iω< th=""></r<iω<></th></f<>	R≤500mΩ	500mΩ <r<iω< th=""></r<iω<>			
RL0603 0mΩ≤R< Ω	±1,500 ppm/°C ±		I,200 p	pm/°C	±800	±800 ppm/°C				
	I0mΩ≤R≤I8mΩ	I8mΩ <r≤47mω< th=""><th>47mΩ</th><th>2<r≤9imω< th=""><th>91mΩ<r≤360mω< th=""><th>360mΩ<r≤500mω< th=""><th>500mΩ<r<iω< th=""></r<iω<></th></r≤500mω<></th></r≤360mω<></th></r≤9imω<></th></r≤47mω<>	47mΩ	2 <r≤9imω< th=""><th>91mΩ<r≤360mω< th=""><th>360mΩ<r≤500mω< th=""><th>500mΩ<r<iω< th=""></r<iω<></th></r≤500mω<></th></r≤360mω<></th></r≤9imω<>	91mΩ <r≤360mω< th=""><th>360mΩ<r≤500mω< th=""><th>500mΩ<r<iω< th=""></r<iω<></th></r≤500mω<></th></r≤360mω<>	360mΩ <r≤500mω< th=""><th>500mΩ<r<iω< th=""></r<iω<></th></r≤500mω<>	500mΩ <r<iω< th=""></r<iω<>			
RL0805	±1,500 ppm/°C	±1,200 ppm/°C	±1,00)0 ppm/°C	±600 ppm/°C	±300 ppm/°C	±200 ppm/°C			
RLI206	±1,500 ppm/°C	±1,200 ppm/°C	±1,00)0 ppm/°C	±600 ppm/°C	±300 ppm/°C	±200 ppm/°C			
10mΩ≤R<1Ω	±1,500 ppm/°C	±1,000 ppm/°C	±80	0 ppm/°C	±600 ppm/°C	±300 ppm/°C	±200 ppm/°C			
RL2010	±1,500 ppm/°C	±1,200 ppm/°C	±1,00)0 ppm/°C	±600 ppm/°C	±300 ppm/°C	±200 ppm/°C			
RL2512	±1,500 ppm/°C	±1,200 ppm/°C	±80	0 ppm/°C	±600 ppm/°C	±300 ppm/°C	±200 ppm/°C			
RLI2I8 ∣0mΩ≤R<∣Ω	I0mΩ≤R≤30mΩ	Ω 30mΩ <r≤56< th=""><th>δmΩ</th><th>56mΩ<r≤< th=""><th>I 80mΩ</th><th>180mΩ<r<1ω< th=""><th>2</th></r<1ω<></th></r≤<></th></r≤56<>	δmΩ	56mΩ <r≤< th=""><th>I 80mΩ</th><th>180mΩ<r<1ω< th=""><th>2</th></r<1ω<></th></r≤<>	I 80mΩ	180mΩ <r<1ω< th=""><th>2</th></r<1ω<>	2			
	±2,000 ppm/°C	±1,000 ppm	n/°C	±700 pp	m/°C	±250 ppm/°C				

FOOTPRINT AND SOLDERING PROFILES

For recommended footprint and soldering profiles, please see the special data sheet "Chip resistors mounting".

PACKING STYLE AND PACKAGING QUANTITY

 Table 3
 Packing style and packaging quantity

PACKING STYLE	REEL DIMENSION	RL0402	RL0603	RL0805	RL1206	RL1210	RL1218	RL2010	RL2512
Paper taping reel (R)	7" (178 mm)	10,000	5,000	5,000	5,000	5,000			
	10" (254 mm)	20,000	10,000	10,000	10,000	10,000			
	13" (330 mm)	50,000	20,000	20,000	20,000	20,000			
Embossed taping reel (K)	7" (178 mm)						4,000	4,000	4,000

ΝΟΤΕ

I. For paper/embossed tape and reel specification/dimensions, please see the special data sheet "Chip resistors packing".

Chip Resistor Surface Mount RL

0402 to 2512 (RoHS Compliant)

SERIES

FUNCTIONAL DESCRIPTION

OPERATINGTEMPERATURE RANGE

Range: -55 °C to +125 °C

POWER RATING

Each type rated power at 70 °C: RL0402=1/16 W; RL0603=1/10 W; RL0805=1/8 W; RL1206=1/4 W; RL1210=1/2 W; RL1218=1 W; RL2010=3/4 W; RL2512=1 W.

RATED VOLTAGE

The DC or AC (rms) continuous working voltage corresponding to the rated power is determined by the following formula:

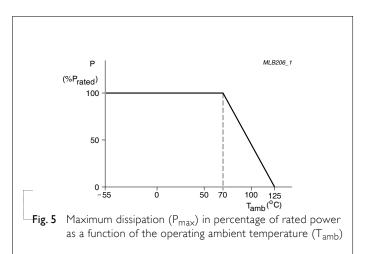
$$V = \sqrt{P \times R}$$

Where

V = Continuous rated DC or AC (rms) working voltage (V)

P = Rated power (W)

 $R = Resistance value (\Omega)$



Chip Resistor Surface Mount RL SERIES 0402 to 2512 (RoHS Compliant)

9

TESTS AND REQUIREMENTS

Table 4 Test condition, procedure and requirements

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Life/	MIL-STD-202G-method 108A	I,000 hours at 70±5 °C applied RCWV	±2%
Operational Life/	IEC 60115-1 4.25.1	1.5 hours on, 0.5 hour off, still air required	
Endurance	JIS C 5202-7.10		
High	MIL-STD-202G-method 108A	1,000 hours at maximum operating temperature	±1%
Temperature Exposure/	IEC 60115-1 4.25.3	depending on specification, unpowered	
Endurance at upper category temperature	JIS C 5202-7.11	No direct impingement of forced air to the parts Tolerances: I25±3 °C	
Moisture Resistance	MIL-STD-202G-method 106F IEC 60115-1 4.24.2	Each temperature / humidity cycle is defined at 8 hours (method 106F), 3 cycles / 24 hours for 10d with 25 °C / 65 °C 95% R.H, without steps 7a & 7b, unpowered	±2%
		Parts mounted on test-boards, without condensation on parts	
		Measurement at 24±2 hours after test conclusion	
Thermal Shock	MIL-STD-202G-method 107G	-55/+125 ℃	±1%
		Note: Number of cycles required is 300. Devices unmounted	
		Maximum transfer time is 20 seconds. Dwell time is 15 minutes. Air – Air	
Short time	MIL-R-55342D-para 4.7.5	2.5 times RCWV or maximum overload voltage	±2%
overload	IEC60115-14.13	whichever is less for 5 sec at room temperature	No visible damage
Board Flex/	IEC60115-1 4.33	Device mounted on PCB test board as described,	±1%
Bending		only I board bending required	No visible damage
		3 mm bending	
		Bending time: 60±5 seconds	
		Ohmic value checked during bending	

Chip Resistor Surface Mount RL SERIES 0402 to 2512 (RoHS Compliant)

Product specification 8

J	0
1	9

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS	
Solderability				
- Wetting	IPC/JEDECJ-STD-002B test B IEC 60068-2-58	Electrical Test not required	Well tinned (≥95% covered) No visible damage	
		Magnification 50X		
		SMD conditions:		
		I st step: method B, aging 4 hours at 155 °C dry heat		
		2^{nd} step: leadfree solder bath at 245±3 °C		
		Dipping time: 3±0.5 seconds		
- Leaching	IPC/JEDECJ-STD-002B test D	Leadfree solder, 260 °C, 30 seconds	No visible damage	
-	IEC 60068-2-58	immersion time		
- Resistance to	MIL-STD-202G-method 210F	Condition B, no pre-heat of samples.	±1%	
Soldering Heat	IEC 60068-2-58	Leadfree solder, 270 °C, 10 seconds immersion time	No visible damage	
		Procedure 2 for SMD: devices fluxed and cleaned with isopropanol		

YAGEO	Phicomp		

Chip Resistor Surface Mount RL SERIES 0402 to 2512 (RoHS Compliant)

9

REVISION HISTORY

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 4	Dec 11, 2008	-	- Halogen Free Epoxy
			- Update global part number definition
Version 3	Aug 07, 2008	-	- Change to dual brand datasheet that describe RL0402 to RL2512 with RoHS compliant
			- Define global part number
Version 2	Jul 15, 2005	-	- Ordering example revised
Version I	Apr 15, 2005	-	- Size 1218 extended
			- Test method and procedure updated
			- PE tape added (paper tape will be replaced by PE tape)
Version 0	Nov. 10, 2003	-	- First issue of this specification

"Yageo reserves all the rights for revising the content of this datasheet without further notification, as long as the products itself are unchanged. Any product change will be announced by PCN."